

UC 10699 09F

CBM003 ADD/CHANGE FORM

APPROVED FEB 24 2010

Undergraduate Council
 New Course Course Change
 Core Category: NONE Effective Fall 2010

or

Graduate/Professional Studies Council
 New Course Course Change
 Effective Fall

RECEIVED OCT 16 2009

MB

- Department: ECE College: ENGR
- Faculty Contact Person: John Glover Telephone: X34430 Email: glover@uh.edu
- Course Information on New/Revised course:
 - Instructional Area / Course Number / Long Course Title:
ECE / 5367 / Introduction to Computer Architecture and Design
 - Instructional Area / Course Number / Short Course Title (30 characters max.)
ECE / 5367 / INTRO TO COMPUTER ARCH & DSGN
 - SCH: 3.00 Level: SR CIP Code: 14.0901.00.06 Lect Hrs: 3 Lab Hrs: 0
- Justification for adding/changing course: **To provide flexibility in scheduling**
- Was the proposed/revised course previously offered as a special topics course? Yes No
 If Yes, please complete:
 - Instructional Area / Course Number / Long Course Title:
 / /
 - Course ID: Effective Date (currently active row):
- Authorized Degree Program(s): BSEE Electrical Engineering, BSCpE Computer Engineering
 - Does this course affect major/minor requirements in the College/Department? Yes No
 - Does this course affect major/minor requirements in other Colleges/Departments? Yes No
 - Can the course be repeated for credit? Yes No (if yes, include in course description)
- Grade Option: Letter (A, B, C ...) Instruction Type: lecture ONLY (Note: Lect/Lab info. must match item 3, above.)
- If this form involves a change to an existing course, please obtain the following information from the course inventory: Instructional Area / Course Number / Long Course Title
ECE / 5367 / Introduction to Computer Architecture and Design
 - Course ID: 18912 Effective Date (currently active row): 20033
- Proposed Catalog Description: (If there are no prerequisites, type in "none".)
 Cr: 3. (3-0). Prerequisites: ECE 3441 and credit for or concurrent enrollment in ECE 4436. Description (30 words max.): Computer organization, computer arithmetic, instruction sets, programming with MIPS assembly language, CPU design, pipelining, memory hierarchy including caching and virtual memory.
- Dean's Signature: David P. Shattuck Date: 11 Oct 2009
 Print/Type Name: David P. Shattuck

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