UC 9066 06F

CBM003 ADD/CHANGE FORM

☒ Undergraduate Council	or	☐ Graduate/Professional Studies Council
☐ New Course ☒ Course Change		☐ New Course ☐ Course Change
Core Category: ____ Effective Fall 2007		Effective Fall __

RECEIVED OCT 12 2006

APPROVED FEB 21 2007

1. Department: COSC College: NSM
2. Person Submitting Form: Venkat Subramaniam Telephone: 33342
3. Course Information on New/Revised course:
   - Instructional Area / Course Number / Long Course Title:
     COSC / 3430 / Computer Architecture
   - Instructional Area / Course Number / Short Course Title (30 characters max.)
     COSC / 3430 / COMPUTER ARCHITECTURE
   - SCH: 4.00 Level: JR CIP Code: 11.0701.00 06 Lect Hrs: 3 Lab Hrs: 3
4. Justification for adding/changing course: **To enable better course content delivery**
5. Was the proposed/revised course previously offered as a special topics course? ☐ Yes ☒ No
   If Yes, please complete:
   - Instructional Area / Course Number / Long Course Title:
     _____ / _____ / _____
   - Content ID: _____ Start Date (yyyy3): _____
6. Is this course offered for undergraduate credit only? ☒ Yes ☐ No
7. Authorized Degree Program(s): B.S., COMPUTER SCIENCE
   - Does this course affect major/minor requirements in the College/Department? ☒ Yes ☐ No
   - Does this course affect major/minor requirements in other Colleges/Departments? ☐ Yes ☒ No
   - Are special fees attached to this course? ☐ Yes ☒ No
   - Can the course be repeated for credit? ☐ Yes ☒ No
8. Grade Option: Letter (A, B, C ...) Instruction Type: lecture/laboratory
9. If this form involves a change to an existing course, please obtain the following information from the course inventory: Instructional Area / Course Number / Long Course Title
   COSC / 3330 / COMPUTER ARCHITECTURE
   - Start Date (yyyy3): _____ Content I.D.: 4085
10. Proposed Catalog Description: (If there are no prerequisites, type in "none".)
    Cr: 4 (3-3). Prerequisites: COSC 2410 and MATH 3336 Description (30 words max.): Logic design, principles of operation of digital computer and analysis of its major components: arithmetic, memory, control and input/output units, instruction pipelining, SIMD and multiprocessor systems; lab simulation of architecture.
11. Dean's Signature: [redacted] 6 Oct 06 Date: _____
    Print/Type Nar[redacted]