

---

# OLIVER R. FASTERLING

---

Highly skilled and enthusiastic Electrical and Computer Engineering major with a Mathematics minor, along with demonstrated abilities in computer programming, hardware, software, networks, and graphic design.

## HIGHLIGHTS

- Languages: English and Spanish (native). Basic knowledge of German and Italian.
- Extensive experience in computer programming and systems management.
- Strong computer troubleshooting, installation and configuration skills.
- Greatly skilled in graphic design, publishing, documentation, website design and management.
- Detail-oriented, multi-tasker with exceptional organizational abilities.
- Distinguished level of ethics, professionalism, quality and ability of committing to deadlines.
- Experienced in project management, incorporated leadership roles. Excellent speaking and writing skills.
- Attentive to details. Maintain meticulous records of all projects.
- Function well in fast-paced, high-pressure environments. Quick study, enjoy new challenges.

## TECHNICAL SKILLS

**Operating Systems:** Microsoft: DOS v3.3 – 6.2, Windows 3.0, 95 – XP, NT 3.5 – 4.0, 2003; UNIX: Linux, Solaris, AIX, Darwin, TRU64, FreeBSD; Apple Macintosh 7.6.1 – MacOS 10.4.2.

**Programming Languages:** C, C++, Java, Visual BASIC, FORTRAN, HTML, JavaScript, ASP, PHP, Verilog, VHDL.

**Software Applications:** Microsoft: Visual Studio, Office, Publisher, FrontPage, Project, VISIO, Exchange Server. Autodesk: AutoCAD, 3D Studio MAX, formZ. Adobe: Photoshop, Illustrator, InDesign, PageMaker, Acrobat. Macromedia: Action!, Director, Freehand, Dreamweaver, Fireworks; QuarkXPress. Databases: MySQL, MS SQL Server 2000, Oracle, DB2, Access.

**Hardware:** Skilled in assembly, troubleshooting, and analysis of many types of electronic devices and components (e.g. computers, printers, scanners, motherboards, RAM, HDD, NIC, interface cards, sound cards, modems, SCSI-cards, power supplies, multi-meters, oscilloscopes, resistors, capacitors, inductors, regulators, op-amps, diodes, etc.). A+ Certification holder.

## EDUCATION

**University of Houston**, Houston, Texas Aug 1998 – Dec 2005  
Bachelor of Science in Electrical and Computer Engineering. Minor in Mathematics.  
**Graduation date: December 2005**  
Honor's College: Fall 1998 – Spring 2000  
Dean's List: Fall 2000, Fall 2001, Spring 2002, and Spring 2003. Honor Roll: Spring 2001, Fall 2002, and Fall 2003.  
**Cumulative GPA: 3.22 / 4.00**

**NHS** – Northbrook High School (SBISD), Houston, Texas (top 10% class graduate) Aug 1996 – May 1998

**ITESM** – Instituto Tecnológico y de Estudios Superiores de Monterrey, Mexico Aug 1994 – May 1996  
Private high school on 45% tuition scholarship. College level curriculum.

## SCHOLARSHIPS AND AWARDS

Rincón Hispanic Scholarship Foundation, Houston, Texas Aug 2002 – Dec 2005  
Houston Louis Stokes Alliance for Minority Participation (HLSAMP), University of Houston Aug 2000 – Dec 2005  
Academic Achievers Scholarship, Center for Mexican American Studies, University of Houston Aug 1998 – Dec 2005  
The Childress Foundation Scholarship, Houston, Texas Aug 1998 – May 2003

## MEMBERSHIPS AND ACTIVITIES

**SGA** – Student Government Association, University of Houston Apr – Dec 2005  
Elected office: Senator for the Cullen College of Engineering.

**SHPE** – Society of Hispanic Professional Engineers, UH Student Chapter Jan 2002 – Dec 2005  
Officer positions held: Historian (2003-2004), Webmaster: www.uh.edu/shpe (2003-2004).  
Chairperson: RLC 2005 – Regional Leadership Conference, Region V.  
Ran for President for the 2004-2005 academic year.

**MAES** – Society of Mexican American Engineers and Scientists, UH Student Chapter Aug 2000 – Dec 2005  
Officer position held: Webmaster: www.uh.edu/maes (2004).

**IEEE** – Institute of Electrical and Electronics Engineers (IEEE), UH Student Chapter Aug 2000 – Dec 2005

## EXPERIENCE

International Business Machines Corporation (IBM)  
Austin, Texas

May – Aug 2005

*Co-op/Intern*

Summer internship. Worked for the IBM Global Services division in a technical position involving Test Automation for the e-ESM 6.3.2 (e-business Enterprise Systems Management) suite. e-ESM is a collection of processes and tools developed to support four IBM Global Services customer service center (help desk) related disciplines: call, problem, change, and asset management. The test automation consisted of developing scripts in Java using the IBM Rational Software Development Platform 6. The main purpose was to automate executions of Test Cases applied to GUI (Graphical User Interface) applications, including the Tivoli Service Desk and ManageNow suites. An additional task involved SQL scripting on DB2 databases, which contained the e-ESM infrastructure. Configured and assembled application servers (e.g. xSeries 342), database servers (e.g. Netfinity 6000R), and different operating systems (AIX, Red Hat Linux, and Windows 2003 Server).

Dell Inc.  
Round Rock, Texas

May – Aug 2004

*Intern*

Summer internship. I was involved in the Research and Development section of the company (Product Group), working for the Server Performance Analysis Laboratory (SPA Lab). Duties included the development of a brand new benchmark tool whose main purpose was to measure the Total Operations Per Second (TOPS) of a variety of prototype servers. The language utilized in coding this tool was Java 2 Enterprise Edition (J2EE 1.4). The setup would consist of large scale systems, with several application servers running MS Windows 2003 Server and Red Hat Linux Enterprise Server, database servers running MS SQL 2000 and Oracle, and several drivers (clients) running Windows and/or Linux. My main objective was to deploy the benchmark (jAppServer2004) on a stable system configuration and to submit the scores for publishing to the Standard Performance Evaluation Corporation (SPEC).

Texas Center for Superconductivity at the University of Houston (T<sub>C</sub>SUH)  
University of Houston, Houston, Texas

July 2002 – Dec 2005

*IT Administrator and Research Assistant in HTS Applications*

Worked as a systems analyst and administrator where I provided on-site support to the network and workstations of the center. In one main project, I was appointed to lead the complete design and cost analysis of the audio/visual and computer equipment utilized at the 2002 International Applied Superconductivity Conference held in Houston, TX at the George R. Brown convention center. 1,500 attendees. As an additional task, I am involved in the research of visualization of magnetic flux of high temperature superconductors (HTS). This research project had as one of its main objectives the development of an effective image processing algorithm that can process large amounts of binary data in the form of pixels. The output of this algorithm established the graphical pattern of the critical current ( $J_C$ ) flowing through the superconductor being analyzed. Theoretical analysis: Faraday's Polarization effect, Biot-Savart Law and Fourier transforms.

Program for Mastery in Engineering Studies (PROMES) / Scholar Enrichment Program (SEP)  
University of Houston, Houston, Texas

Aug 2001 – Dec 2005

*Workshop Facilitator/Tutor/Teacher Assistant/Webmaster*

Mentored and assisted an average group of 20 students in a combined course of Linear Algebra and Differential Equations as a teacher assistant. Duties included close monitoring of each student, by providing worksheets of related material and fostering group-work habits. The workshop had as one of its main goals for the students to achieve the best possible grades in their respective lectures. Received repeated accolades from the program managers and directors for utmost dedication, professionalism in my duties and achieving leading results in behalf of the students.

Schlumberger  
Sugar Land, Texas

Aug 2000 – July 2002

*Intern/Computer and Electronics Specialist*

Two-year internship in which I performed a variety of duties, all related to my career objective. Electronics: Served as an electronics specialist to provide manufacturing aid on prototype designs that are geared for technological advancement of the company. Duties included: Soldering, wiring, crimping and complete schematic build-up of electrical components. Computer aided drafting and design using AutoCAD 2000. Electrical and mechanical drafting of schematics, involving hydraulics, electrical components, trucks and offshore platforms. Developed a number of software applications, using MS Visual Basic 6.0, which provided access to field engineers to Real-time Data Delivery (RDD) information, in which the viewer could produce instantaneous reports of data acquisition and jobs made from a remote location. Web design: Created a dynamic website to establish a dynamic access to documentation of services and products offered to field engineers and third-party vendors.

Texas Instruments Incorporated  
Stafford, Texas

Jan – May 2000

*Co-op/Intern*

Five-month internship. Developed a database system utilizing programming skills in Visual Basic and MS Access. The database would gather experimental data from a silicon wafer laboratory, using a Hitachi SEM (Scanning Electron Microscope) device to process quality control charts and reports. Secondary duties included the creation of a web-based technical support page, which served as a help page for users of the database mentioned earlier. Learned valuable project management techniques.