

UC 10951 10F

CBM003 ADD/CHANGE FORM

APPROVED NOV 17 2010

Undergraduate Council  
 New Course  Course Change  
 Core Category: NONE Effective Fall 2011

or

Graduate/Professional Studies Council  
 New Course  Course Change  
 Effective Fall 2011

1. Department: Electrical and Computer Engineering College: ENGR  
 2. Faculty Contact Person: Yuhua Chen Telephone: 713-743-4441 Email: yuhua.chen@mail.uh.edu

3. Course Information on New/Revised course:  
 • Instructional Area / Course Number / Long Course Title:  
ECE / 5440 / Advanced Digital Design  
 • Instructional Area / Course Number / Short Course Title (30 characters max.)  
ECE / 5440 / ADVANCED DIGITAL DESIGN  
 • SCH: 4.00 Level: SR CIP Code: 14.1001.00 06 Lect Hrs: 3 Lab Hrs: 3

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4. Justification for adding/changing course: **To reflect change in prerequisite course**  
 5. Was the proposed/revised course previously offered as a special topics course?  Yes  No  
 If Yes, please complete:

- Instructional Area / Course Number / Long Course Title:  
 \_\_\_\_ / \_\_\_\_ / \_\_\_\_
- Course ID: \_\_\_\_ Effective Date (currently active row): \_\_\_\_

6. Authorized Degree Program(s): BSEE, BSCpE
- Does this course affect major/minor requirements in the College/Department?  Yes  No
  - Does this course affect major/minor requirements in other Colleges/Departments?  Yes  No
  - Can the course be repeated for credit?  Yes  No (if yes, include in course description)

7. Grade Option: Letter (A, B, C ...) Instruction Type: lecture laboratory (Note: Lect/Lab info. must match item 3, above.)

8. If this form involves a change to an existing course, please obtain the following information from the course inventory: Instructional Area / Course Number / Long Course Title

ECE / 5440 / Advanced Digital Design  
 • Course ID: 018923 Effective Date (currently active row): 08/24/1998

9. Proposed Catalog Description: (If there are no prerequisites, type in "none".)  
 Cr: 4. (3-3). Prerequisites: ECE 3155, 3355, 3441, and credit for or concurrent enrollment in ECE 4436.  
 Description (30 words max.): Design fundamentals and techniques using application specific integrated circuit development and synthesis tools and field programmable gate arrays. Design of control units, arithmetic and logic units, memory and I/O subsystems, and cache.

10. Dean's Signature: [Signature] Date: 13 Oct 2010

Print/Type Name: Dr. David P. Shattuck